

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 6,941,492 B1  
DATED : September 6, 2005  
INVENTOR(S) : Josef Ezra et al.

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It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 1,

Line 61, "associating" should read -- associated --.

Column 5,

Line 25, "various host system a bus 78," should read -- host system via a bus 78, --.

Column 6,

Line 1, "(referred to as a "global memory")." should read -- (referred to as a "global memory"). --.

Line 29, "also communicates" should read -- also communicate --.

Signed and Sealed this

Twenty-eighth Day of February, 2006



JON W. DUDAS  
*Director of the United States Patent and Trademark Office*